

PTO
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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 100571E5	FILING DATE 01/25/2002	CLASS 361	SUBCLASS 112	GAU 2834	EXAMINER Bancroft
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**APPLICANTS: Adler Eric;

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A DIV OF 09/616,951 07/14/2000 PAT 6,344,964

BEST AVAILABLE COPY

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☐ no

35 USC 119 conditions met ☐ yes ☐ no

Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

BUR919990222US2

TITLE : Capacitor having sidewall spacer protecting the dielectric layer and method of fabricating the same

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-34)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner		CLAIMS ALLOWED	
				Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner		DRAWING	
Amount Due	Date Paid			Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		Application Examiner	
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